Real-Time Estimation and Damping of SSR in a VSC-HVDC Connected Series-Compensated System

Tibin Joseph, Member, IEEE, Carlos E. Ugalde-Loo, Member, IEEE, Senthoooran Balasubramaniam, Student Member, IEEE, and Jun Liang, Senior Member, IEEE

Abstract—Infrastructure reinforcement using high-voltage direct-current (HVDC) links and series compensation has been proposed to boost the power transmission capacity of existing ac grids. However, deployment of series capacitors may lead to subsynchronous resonance (SSR). Besides providing bulk power transfer, voltage source converter (VSC) based HVDC links can be effectively used to damp SSR. To this end, this paper presents a method for the real-time estimation of the subsynchronous frequency component present in series-compensated transmission lines—key information required for the optimal design of damping controllers. A state-space representation has been formulated and an eigenvalue analysis has been performed to evaluate the impact of a VSC-HVDC link on the torsional modes of nearby connected thermal generation plants. Furthermore, the series-compensated system has been implemented in a real-time digital simulator and connected to a VSC-HVDC scaled-down test-rig to perform hardware-in-the-loop tests. The efficacy and operational performance of the ac/dc network while providing SSR damping is tested through a series of experiments. The proposed estimation and damping method shows a good performance both in time-domain simulations and laboratory experiments.

Keywords—subsynchronous resonance, HVDC, eigenanalysis, series compensation, real-time estimation, voltage source converter.

I. INTRODUCTION

SERIES compensation is a well-known method to enhance thermal and stability limits of long transmission lines. However, its use brings the potential risk of subsynchronous resonance (SSR), where the electrical modes of compensated lines interact with the mechanical modes of nearby thermal generation plants [1]. This interaction may result in torsional oscillations on the shafts of generators which, if left unattended, may lead to fatigue and ultimately failure [1]–[3].

Transmission network reinforcement using HVDC links is now a well-established alternative to series compensation. This technology has proven to be reliable, providing advanced functionality to network operators [4], [5]. Compared to line commutated converter (LCC) based HVDC, the voltage source converter (VSC) based technology is gaining momentum due to its inherent advantages [6]. VSC-HVDC links are being connected in parallel with existing LCC links to further enhance the power transfer capability of already compensated ac systems [4], [5]. Although it has been shown that LCC-HVDC systems connected to turbo-generating units may destabilize torsional modes and lead to torsional interactions [7], this is not the case for most operating conditions of VSC-HVDC systems [8]. However, destabilization of torsional dynamics using VSC-HVDC has been also reported [9], [10] and, thus, its impact on torsional modes has to be carefully evaluated before exploring its capability for SSR damping. SSR has been lately reported in wind turbines and series-compensated transmission lines; thus, SSR monitoring and damping are still of great practical significance [11], [12]. Recent research has recognized this, with a number of monitoring and detection schemes [13], [14], [17] and techniques to damp SSR [18]–[24] being proposed.

Phasor measurement units (PMUs) have been proposed for the online monitoring and detection of subsynchronous torsional oscillations [13], [14]. The generator speed is used as an input signal for the damping schemes, which requires communication with the compensating devices. This may affect the reliability of the damping controller as system stability could be compromised under communication delays. Research efforts have been made to account for time delays in transient stability studies. For instance, nonlinear control strategies for communication time delay compensation using PMUs have been proposed to enhance transient stability margins [15], [16]. However, existing PMUs cannot efficiently monitor the wide range of SSR frequencies [5]. To avoid the use of communications, local measurements may be employed to estimate subsynchronous frequency (SSF) components [17], [19], [22]. Potential alternatives include the use of low-pass filter and recursive least square based estimation and damping methods [19], [21]; Kalman filters [17]; and multi-modal damping [22]. It can be concluded from these studies that the use of local measurements for detecting and damping SSF components could be a reliable, cost-effective and robust alternative compared to communication-based methods.

Most PMU or local measurement-based estimation schemes use FACTS devices or HVDC stations for damping purposes [18]–[21]. The use of a static series synchronous compensator (SSSC) and a static synchronous compensator (STATCOM) for SSR damping has been reported in [17], [18], [21]. Recently, VSCs have been used explicitly for providing SSR damping in series-compensated transmission lines [24], [25]. In all these cases the devices were used solely for the purpose of damping without much discussion on their basic functionality and operation under an SSR event. Studies on passive SSR damping schemes and experimental tests have been reported in [25],[26], [29]. In passive schemes, damping is achieved by reshaping the output admittance of the VSC. However, compared with active schemes, passive mitigation methods cannot track and compensate multiple SSF components automatically; thus, they exhibit an inferior SSR suppression performance.
Another aspect which demands attention is the lack of availability of turbo-generator shaft data, which limits the adaptability of damping schemes to multiple modes of oscillations. Real-time estimation and tracking of subsynchronous electrical modes at any series compensation level can help to overcome this issue, preventing interactions with the mechanical shaft modes—irrespective of their values. To contribute to this effort, this paper proposes an adaptive subsynchronous frequency damping controller (SSFDC) capable of estimating and damping SSF components regardless of the configuration of the power system. For completeness, the adaptive damping scheme is tested on a real-time experimental test-rig.

Although [29] constitutes a good attempt to experimentally damp SSR, its practical contribution is limited as the exact knowledge of SSF components is a pre-requisite for a successful performance. The work presented in this paper goes beyond that initial effort by providing an adaptive structure that is capable of identifying, in real-time, SSF components and damping them regardless of their values. In other words, the scheme proposed in this work would be useful even when generator data is not available. In addition, the stability assessment of the whole system, including the GB system, the HVDC link and all controllers, is provided for completeness.

The main contributions of the paper are: (1) quantification of SSR in a series-compensated ac/dc network using a detailed state-space system model and eigenanalysis; (2) design of an adaptive SSFDC which can identify, track and damp multiple SSF modes; (3) unlike previous research work studying SSR using theoretical analysis and time-domain simulations, the present work utilizes a real-time hardware-in-the-loop (RT- HiL) experimental platform for the validation of the SSFDC-equipped VSC-HVDC link. Detailed SSR eigenanalysis is carried out in an ac/dc system resembling the reduced Great Britain (GB) power system. The effectiveness of the proposed SSFDC scheme to damp multiple torsional modes while providing basic functionality of power transfer has been validated through time-domain simulations and RT-HiL experiments.

II. SYSTEM CONFIGURATION AND MODELING

IEEE First and Second Benchmark Models (FBM and SBM) have been traditionally used to obtain a good understanding of SSR [27], [28]. As such, they are suitable for designing SSFDCs [23], [26]. These models have been extended to the network shown in Fig. 1 to assess the impact of VSC-HVDC links in a simplified GB mainland system splitting into three major generation areas: England and Wales (E&W), Southern Scotland (SS) and Northern Scotland (NS), with respective ratings of 21000, 2800 and 2400 MVA [30]. The NS generator model includes a multi-mass shaft with six turbine masses as in the FBM. The remaining machines were modeled as single-mass turbo-generators. The test system has been previously used for SSR studies in a reinforced GB system [29]–[32].

Infrastructure upgrades are in line with those proposed by National Grid (UK’s transmission system operator and owner)—aiming to facilitate the export of additional wind power from Scotland to major load centres in the South without the construction of new overhead lines. Reinforcements consist of series compensation in the inland network and submarine HVDC links between E&W and SS [4], [5]. Although it could be argued that the adopted network is a rather simple version of the GB system, it provides enough information to study, understand and test SSR in a multi-machine ac/dc system.

A. VSC-HVDC Link Control

The primary design objectives of a VSC-HVDC link are active power transfer and reactive power support to the grid. The variables to be controlled are the active and reactive power (at both terminals) and dc voltage. Fig. 2 illustrates the main control strategy for the rectifier station (VSC1) and the dc voltage control at inverter station (VSC2). A dq reference frame is adopted and a cascaded control structure based on PI controllers is used [8], [32]. For modeling details of the VSC-HVDC link the reader is referred to [32], [33]. The control parameters are included in the Appendix for completeness.

B. State-Space Modeling

A detailed state-space model of the system shown in Fig. 1 has been developed. It includes the dynamic equations of the synchronous machines, transmission lines and the HVDC link with its controllers as given by (1)-(4). This is a system of 77 differential equations linearized around an operating point. The state equation and state vector are, respectively, given by

$$\Delta \bar{x}_{sys} = A_{sys} \Delta x_{sys}$$  (1)

$$\Delta x_{sys} = \begin{bmatrix} X_{NG}, X_{NW}, X_{N\theta}, X_{SG}, X_{EWG}, X_{T}, X_{DC} \end{bmatrix}^T$$  (2)

where $X_{NG}, X_{NW}, X_{N\theta}$, constitute the states associated with the NS generator; $X_{T}$, the series-compensated transmission line; $X_{SG}, X_{EWG}$, the SS and E&W generators; and $X_{DC}$, the VSC-HVDC link. The transmission line connecting the NS generator has been upgraded with series compensation. The VSC-HVDC link is connected between Buses 2 and 4.

System (1) has been constructed in MATLAB to perform eigenanalysis. It is characterized by four unstable torsional modes (TM1-TM4) associated with the NS generator shaft which may be excited at different compensation levels. Fig. 3 shows the ranges of compensation for which torsional instability arises. Stability is directly related to the subsynchronous mode (SUB) of the HVDC-connected series-compensated
transmission line linked to the NS generator. Instability occurs when SUB is in the neighborhood of a TM frequency, as shown by the real part of the eigenvalues of TM1-TM4. For further information on system modeling, the reader is referred to [30]–[32], where complete linearized models of the synchronous generator with a multi-mass shaft and a VSC-HVDC link with its controllers are provided. Due to space limitations, the full state-space model is not included in the paper.

Table I summarizes the relevant eigenvalues for 30, 40 and 75% series compensation. As it can be seen, when 30% of compensation is employed the system is stable. However, for 40% SUB (∼ 30.7 Hz) interacts with TM4 of the shaft (∼ 29.6 Hz). As a consequence, SSR arises and TM4 becomes unstable, evidenced by its positive real part. Similarly, for 75% SUB (∼ 23.61 Hz) interacts with TM3 of the shaft (∼ 23.37 Hz), resulting in unstable oscillations in the system evidenced by the positive real part of TM3. For these operating conditions any small disturbance or load change would trigger SSR.

<table>
<thead>
<tr>
<th>TM</th>
<th>30% Compensation</th>
<th>40% Compensation</th>
<th>75% Compensation</th>
</tr>
</thead>
<tbody>
<tr>
<td>TM4</td>
<td>−0.207 ± j185.488</td>
<td>+0.097 ± j187.925</td>
<td>−0.185 ± j185.099</td>
</tr>
<tr>
<td>TM3</td>
<td>−0.383 ± j146.666</td>
<td>−0.379 ± j146.566</td>
<td>+0.029 ± j146.825</td>
</tr>
<tr>
<td>TM2</td>
<td>−0.058 ± j115.970</td>
<td>−0.058 ± j114.75</td>
<td>−0.051 ± j116.012</td>
</tr>
<tr>
<td>TM1</td>
<td>−0.149 ± j90.574</td>
<td>−0.147 ± j91.123</td>
<td>−0.028 ± j90.868</td>
</tr>
<tr>
<td>SUB</td>
<td>−7.443 ± j209.289</td>
<td>−7.674 ± j191.574</td>
<td>−7.735 ± j148.310</td>
</tr>
<tr>
<td>SUPER</td>
<td>−8.518 ± j418.520</td>
<td>−8.665 ± j432.55</td>
<td>−9.110 ± j479.435</td>
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</table>

Fig. 3. Stability of the torsional modes in terms of series compensation. Dashed lines are used for an HVDC-upgraded system.

III. DAMPING CONTROLLER

To damp SSR upon occurrence, a supplementary SSFDC has been designed and integrated with the control loop of VSC1. Its main objective is to increase the network damping at critical frequencies of the turbo-generator shaft. This is achieved by suppressing the line current component at SSFs (see Fig. 2). Fig. 4 shows a block diagram representation of the SSFDC. It is formed by an SSF tracker (SSFT), which estimates and tracks the frequency component in the line current upon an SSR event, and by an SSF damper (SSFD), which utilizes the tracked frequency component to generate a 180° anti-phase signal to be injected at the point of common coupling (PCC) with the VSC station. In general a voltage signal with a SSF component can be represented as:

\[ s(t) = A_1 \sin(2\pi ft + \theta_1) + \sum_{k=2}^{m} A_k \sin(2\pi f_k t + \theta_k) \]

where \( f \) is the fundamental frequency; \( A_k, \theta_k \), the amplitude and phase angle of the \( k \)-th component, which includes SUB and super-synchronous (SUPER) components; and \( m \) the total number of torsional modes. In (3), \( x(t) \) is the fundamental frequency signal and \( g(t) \) encapsulates the SSF component of \( s(t) \). As explained in [21], the network exhibits a small positive damping for frequencies above the fundamental. Therefore, the SUPER component is not of interest. The SSFDC aims to cancel \( g(t) \) from \( s(t) \) to obtain \( x(t) \). In the proposed method, the frequency component of \( g(t) \) is first detected and tracked upon occurrence of SSR. Then a compensation signal \( g'(t) \) is generated to cancel out \( g(t) \).

Fig. 4. Block diagram of the proposed SSFDC: (a) SSFT; (b) SSFD.

A. Subsynchronous Frequency Tracker

During an SSR event, the frequency of the line current will deviate from its nominal value and will begin to oscillate. When the frequency of oscillation \( f_{osc} \) coincides with the complement of the frequency of a generator shaft torsional mode \( f_{tm} \), the oscillations will be sustained [2], [3]. An SSF can be designed to identify and track \( f_{osc} \).

As seen from Fig. 4(a), the SSF is comprised mainly of a SSF component estimator (SSF-E) and a decision block. The line current is fed to the SSF-E through a low-pass filter, which isolates \( g(t) \) and eliminates driving frequency component \( x(t) \) at 50 Hz. This is the input signal to the SSF block, which through a series of transformations identifies the SSF component \( f_i \) in the line current. Once this network mode is identified, a decision block is used to track the magnitude of \( g(t) \). This is compared with \( f_i \) to avoid a tracking operation when no oscillation is observed. An operating mode selector is utilized to enable the output of SSF; which is the tracked oscillation frequency \( f_{osc} \). It should be emphasized that the tracking of \( f_{osc} \) could be done regardless of the configuration.
of the power system since the natural frequencies of a turbo-generator shaft can be considered as constant [2].

B. Subsynchronous Frequency Damper

The second stage in the SSFDC design process is to generate signal $g'(t)$ to eliminate the SSF component ($f_{osc}$). To serve this purpose, the SSFD shown in Fig. 4(b) has been designed and implemented. It is formed by a variable frequency filter and a damper gain. The output of the SSFT is used as the central frequency of the filter, whose input is the $dq$ voltage at the PCC—obtained using a suitable reference frame transformation block and synchronized using a phase-locked loop (PLL). The filter outputs the target signal fed to the damper gain block to produce a 180° phase shift to create $g'(t)$. Following a coordinate transformation, damping components $v_{dssr}$ and $v_{qssr}$ are generated. These are added to the primary control voltage reference signals $v_d$ and $v_q$ of VSC1 (see Fig. 2). The resultant voltage at the converter terminals will damp SSR in the ac network by effectively eliminating $g'(t)$.

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IV. RT-HiL Experimental Set-up

It is difficult to investigate and test SSR damping schemes in practical power systems. However, a good approximation can be achieved using the RT-HiL experimental platform shown in Fig. 5. It consists of a real-time digital simulator (RTDS), a grid simulator (GS), and an HVDC test-rig. To perform RT-HiL tests, the system shown in Fig. 1 was modeled in the RSCAD software of the RTDS. High voltages were converted to a low voltage using the GS, which were in turn interfaced with the HVDC test-rig through the analogue output (AO) cards of the RTDS firmware. The GS produces a three-phase mains supply voltage from the GTA0 cards of the RTDS. This is achieved by using a four-quadrant amplifier rated at 2 kVA and 270 V (line-to-ground rms). To close the loop between the RTDS and the HVDC rig, the three-phase line current is tapped and fed to the analogue input (AI) card of the RTDS (see Fig. 5(a)).

The HVDC test-rig is formed by two VSCs, two transformers, a dc line cabinet and a dSPACE DS1005 controller. The VSCs are operated at a rated power of 1 kW, 140 V ac and ±125 V dc. A conversion ratio of 400 kV / 140 V is achieved using the GS. The output voltage of the GS is controlled using an autotransformer. The dSPACE platform acquires data and monitors system states of the test-rig and controls each VSC. The hardware set-up is shown in Fig. 5(b). The RTDS and VSC-HVDC test-rig parameters are summarized in Table III.

![Figure 5: RT-HiL configuration](image-url)

V. RT-HiL Experimental Results

Experiments were carried out on the RT-HiL platform to validate the effectiveness of the SSFDC to provide damping at selected SSFs and to assess its impact on the converter performance. For comparison purposes, the performance of the integrated ac/dc system shown in Fig. 1 was evaluated with and without the use of an SSFDC. To assess system stability, two scenarios have been considered:

- Case A: Change in compensation level from 30 to 40%.
- Case B: Change in compensation level from 30 to 75%.

In both cases, the change in series compensation occurs at $t = 4$ s into the simulation. This is achieved by suitably modifying the value of $X_c$.

A. Integrated AC/DC System without SSFDC

Figs. 6 to 8 show the RT-HiL experimental results for Cases A and B without the use of an SSFDC. The occurrence of SSR is evidenced in Figs. 6(a) and 6(b) by the rise in the magnitude of line current from its rated value. The dominant

<table>
<thead>
<tr>
<th>TABLE II. EIGENVALUES OF THE COMBINED SYSTEM WITH SSFDC.</th>
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<tr>
<td>TM</td>
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<tr>
<td>TM5</td>
</tr>
<tr>
<td>TM4</td>
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<tr>
<td>TM3</td>
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<tr>
<td>TM2</td>
</tr>
<tr>
<td>TM1</td>
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<tr>
<td>SUB</td>
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<tr>
<td>SUPER</td>
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<th>TABLE III. TEST-RIG SPECIFICATIONS AND PARAMETERS</th>
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<tbody>
<tr>
<td>Devices</td>
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<tr>
<td>-----------------</td>
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<tr>
<td>Converters</td>
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<tr>
<td>Topology</td>
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<tr>
<td>AC inductors</td>
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<td>DC lines</td>
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<td>DC capacitors</td>
</tr>
<tr>
<td>Control system</td>
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<td>Real-time simulator</td>
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</table>
SSF components for the new compensation values (≈ 20 Hz for 40% compensation and ≈ 27 Hz for 75% compensation) are evidenced in Figs. 6(c) and 6(d)—obtained using a fast Fourier transform (FFT) spectrum analysis. The information provided by Fig. 6 agrees well with the eigenanalysis presented in Section III-C: at 40% compensation TM4 (≈ 30 Hz) is excited as this is the complement of the network SUB mode (≈ 20 Hz); conversely, at 75% compensation TM3 (≈ 23 Hz) is excited as this is the complement of the corresponding SUB mode (≈ 27 Hz). It is clear from the plots that without proper countermeasure methods, the network SSF component increases in magnitude and the system becomes unstable.

Fig. 7 shows the frequency and torque responses for Case A only. Since the plots for Case B exhibit a similar behavior, no further discussion is warranted. Fig. 7(a) shows the network frequency, which starts deviating from 1 p.u. (50 Hz) due to SSR following the change in series compensation. Fig. 7(b) depicts the selected torque response in the shaft of the NS generator. As it can be observed, the system is unstable as predicted by the eigenanalysis (see Fig. 3 and Table I).

Fig. 8 shows the dq voltages (Vd, Vq) at the PCC, the dc link voltage (Vdc), and the output power (Pd) of VSC1 at Bus 2. The results reveal that the magnitude of these variables increases following the change in compensation. If no corrective measures are taken, these oscillations will surpass the converter limits. Fig. 8(a) shows the converter dq ac voltage. It can be observed that the SSF component is transferred into the ac side voltage. Converter instability is evidenced by the dc voltage and power output (see Figs. 8(b) and 8(c)). In all cases, the oscillations build up over a long period (≈ 16 s), which is a natural result of low-frequency instability associated with SSFs [26]. It can be concluded that the VSC on its own does not provide sufficient system damping following a change in series compensation. Without proper countermeasures, the operation of the ac/dc network could be compromised due to SSR.

B. Integrated AC/DC System with SSFDC

The control loop of VSC1 is upgraded with an SSFDC, with experimental results shown in Figs. 9 to 14. Using the SSF and SSFT described in Section IV, the network SSF component (∼ωosc) for Cases A and B is successfully identified, as shown in Fig. 9. For Case A, the SSFT is able to track the dominant SSF component (∼ 20 Hz) in less than ≈ 2.5 s (see Fig. 9(a)). A similar response is observed for Case B, where the dominant mode (∼ 27 Hz) is also tracked (see Fig. 9(b)). These results agree well with the FFT spectra in Figs. 6(c)-(d) and with the eigenvalues from Table I.

The line current plots and their frequency spectra are shown in Fig. 10. The change in series compensation produces initial increasing oscillations in both cases. However, once the SSFT
identifies and tracks the exact network mode, the SSFDC acts at $t \approx 7$ s into the simulation by injecting the anti-phase signal and, consequently, is able to damp SSR (see Figs. 10(a) and 10(b)). The network SSF components interacting with the mechanical modes are identified, tracked and damped in less than 3.5 s and this is evidenced by Figs. 10(c) and 10(d).

Figs. 11 and 12 show the frequency of the phase $a$ current and selected torque responses of the NS generator. An initial transient caused by the change in series compensation results in a frequency deviation from its nominal value and an increase in torque oscillations. Once the SSFDC operates, the shaft oscillations are damped out and steady-state operation is reached. These plots reveal the successful damping of multiple torsional modes—afforded by a correct SSF component tracking.

Figs. 13 and 14 show the responses of the VSC. The contribution of the SSFDC is evident and the stable performance of the converter is reflected in the ac voltage and its $dq$ components (see Figs. 13(a) and 14(a)). The dc link voltage ($V_{dc}$) and the dc power ($P$) at VSC1 follow the dynamic pattern of the ac system response (see Figs. 13(b)-(c) and 14(b)-(c)). Initially, $V_{dc}$ and $P$ deviate from their nominal values, but once the SSFDC operates the oscillations disappear and steady-state is reached within seconds.

The results presented in this section show that the active damping compensation afforded by the proposed SSFDC stabilizes the system. A good performance is achieved for different modes of oscillation. Moreover, the additional support provided by VSC1 is within the converter capability and thus does not affect the normal operation of the HVDC link.

Although the main purpose of the paper is to provide evidence of a successful experimental performance of the proposed damping scheme, it should be emphasized that the RT-HiL results agree on well with those obtained via simulations. As shown in Figs. 15-16, a good performance is observed for Case A in both instances. This validates the effectiveness of the SSFDC-equipped VSC-HVDC link to provide SSR damping.

C. On the Use of the RT-HiL Platform

HiL enables the development and testing of complex embedded systems in real-time. In an RT-HiL platform, a mathematical representation related to complex system dynamics (in
this case, the GB power system) interacts with the embedded system under study (the VSC-based HVDC link and its controllers). This way, the complexity of the plant under control is fully included in the test. RT-HIL can be used to model dangerous operating conditions—such as SSR occurrence.

By adopting the RT-HIL test-bench shown in Fig. 5, it is possible to assess the suitability of simulation-based models and to validate, in real-time, the feasibility of the damping strategy proposed in this work. Given the disruptive and dangerous nature of SSR and the potential damage that it may cause not only to power system plant, but also to personnel, the approach presented in this paper would enable a transmission system operator to reduce the risk of integrating SSR detection and damping schemes to a practical power system without subjecting it to permanent damage.

VI. DISCUSSION ON ROBUSTNESS AND UNCERTAINTY

A. Impact of Torsional Stiffness Uncertainty on SSF

Parametric variation in spring constants (torsional stiffness) can change the TMs of the turbine-masses and, as a result, shift the resonant frequencies in series-compensated transmission lines [34]. This may affect the tuning of SSFDCs. To account for torsional stiffness uncertainty and to assess its impact on SSF measurement and damping, variations of ±20% to the original spring constant values, denoted \( K_{\text{org}} \), are considered (see Table IV). The eigenvalues for Case A are shown in Table V. For the set of parameters \( K_{\text{max}} \), the SUB mode shifts from 30 to 32 Hz and SSR still arises in the system. Although SUB remains unaffected for set \( K_{\min} \), it can be noticed that TM4 is no longer in the neighborhood of SUB and, as a result, the system is no longer unstable for 40% series compensation.

<table>
<thead>
<tr>
<th>Shaft</th>
<th>( K_{\text{min}} ) (p.u.)</th>
<th>( K_{\text{org}} ) (p.u.)</th>
<th>( K_{\text{max}} ) (p.u.)</th>
</tr>
</thead>
<tbody>
<tr>
<td>HP-LP</td>
<td>15.44</td>
<td>19.303</td>
<td>23.16</td>
</tr>
<tr>
<td>LP-LPA</td>
<td>27.94</td>
<td>34.929</td>
<td>41.91</td>
</tr>
<tr>
<td>LPA-LPB</td>
<td>41.63</td>
<td>52.038</td>
<td>62.44</td>
</tr>
<tr>
<td>LPB-GEN</td>
<td>56.68</td>
<td>70.852</td>
<td>85.02</td>
</tr>
<tr>
<td>GEN-EX</td>
<td>2.25</td>
<td>2.822</td>
<td>3.38</td>
</tr>
</tbody>
</table>

The performance of the proposed SSFDC scheme under torsional stiffness variations is assessed via time-domain simulations. As evidenced by Fig. 17(a), the SSFE scheme is able to identify the new network mode (18 Hz) for parameters \( K_{\text{max}} \), which is the complement of SUB (32 Hz). This demonstrates the capability of the SSFDC scheme in identifying oscillation frequencies under parametric changes. The damping performance for Case A is shown in Fig. 17(b). As it can be seen, the damper is robust enough to cope with the parametric uncertainty while providing a satisfactory performance.

B. SSFDC Performance for Extreme Operating Conditions

Two cases are examined to assess the robustness and performance of the SSFDC under extreme operating conditions. In the first one, a very high compensation value is adopted (OP1), while in the second one a large disturbance is considered (OP2).

In OP1, 110% series compensation is employed. This excites TM1 (14 Hz) of the NS generator (see Fig. 3). Fig. 18(a) shows the SSF component in the line current following a change in series compensation from 30 to 110% at \( t = 4 \) s. It can be seen that the SSFE is able to identify the right SSF component (36 Hz), which is the complement of TM1 (14 Hz). Moreover, the SSFDC is able damp the SSF component, which does not occur in the system without a damper (see Fig. 18(b)).

In OP2, a three-phase-to-ground fault takes place in the series-compensated line. It occurs at \( t = 4 \) s and has a duration of 75 ms. A 40% compensation level is considered. As it can be observed in Fig. 18(a), the SSF component in the line current (20 Hz) is identified after the fault. Without an SSFDC in place, SSR appears following fault clearance, as evidenced by the fluctuations in the dc link power in Fig. 18(c). Conversely, SSR is damped once SSFDC operation is enabled in VSC1.

The results presented in this section confirm the capability of the proposed SSFDC to achieve a robust damping performance upon extreme operating conditions. It should be emphasized that the quantification of robustness margins when multiple uncertainties and/or operating conditions are considered is out of the scope of this work.

C. Impact of Delays on SSFDC Operation

In the proposed SSFDC scheme, the information to be communicated between the damper and the VSC includes the oscillation frequency and locally measured currents. The amount of data to be transferred is small, which implies limited communication time delays. In addition, it should be borne in mind that SSR may take seconds or even minutes to build-up in a practical power system upon disturbances; thus, communication delays in the range of milliseconds will not affect the SSFDC performance [35], [36]. Although there may be delays present in the control loops of the VSCs, the bandwidth of these loops is much higher than the SSFs; thus, the SSR modes will not be affected [10], [36].

To corroborate the previous assessment, the impact of time delays between the occurrence of SSR and its detection on the performance of the proposed SSFDC is studied. A delay policy

<table>
<thead>
<tr>
<th>TM</th>
<th>( K_{\text{min}} )</th>
<th>( K_{\text{org}} )</th>
<th>( K_{\text{max}} )</th>
</tr>
</thead>
<tbody>
<tr>
<td>TM5</td>
<td>(-1.173 \pm j243.44)</td>
<td>(-1.173 \pm j272.183)</td>
<td>(-1.173 \pm j298.170)</td>
</tr>
<tr>
<td>TM4</td>
<td>(-0.20 \pm j165.96)</td>
<td>(+0.097 \pm j187.925)</td>
<td>(+0.075 \pm j193.45)</td>
</tr>
<tr>
<td>TM3</td>
<td>(-0.383 \pm j131.21)</td>
<td>(-0.379 \pm j146.56)</td>
<td>(-0.371 \pm j160.69)</td>
</tr>
<tr>
<td>TM2</td>
<td>(-0.085 \pm j103.69)</td>
<td>(-0.085 \pm j114.75)</td>
<td>(-0.087 \pm j126.991)</td>
</tr>
<tr>
<td>TM1</td>
<td>(-0.151 \pm j81.14)</td>
<td>(-0.147 \pm j91.123)</td>
<td>(-0.144 \pm j99.185)</td>
</tr>
<tr>
<td>SUB</td>
<td>(-7.368 \pm j192.98)</td>
<td>(-7.674 \pm j192.75)</td>
<td>(-7.659 \pm j202.63)</td>
</tr>
<tr>
<td>SUPER</td>
<td>(-8.665 \pm j434.76)</td>
<td>(-8.665 \pm j432.55)</td>
<td>(-8.665 \pm j434.76)</td>
</tr>
</tbody>
</table>

Fig. 17. Simulation plot for spring constant uncertainty.

(continued)
of 1 s for the SSFE is adopted, which is considered reasonable compared to other known methods for SSR detection requiring more than 2 s for SSF estimation [37]. This time is considered as sufficient to accommodate for multiple SSR modes due to measurement and parametric uncertainty.

Fig. 19 shows the simulation results for Case A when different time delays on SSF detection are considered. As it can be observed, a delay of 1 s does not have a severe impact on the damping performance. However, the longer it takes to detect the SSF component, the longer it takes for the torque responses to decrease their magnitudes. It should be highlighted that the damping capability is still preserved even with an arguably large delay time (8 s) in SSF detection—at the expense of increasing torque oscillations for a few seconds.

**D. A Discussion on System Stability**

In this work, stability has been analyzed so far using eigenanalysis. A delay of 1 s for the SSFE is adopted, which is considered reasonable compared to other known methods for SSR detection requiring more than 2 s for SSF estimation [37]. This time is considered as sufficient to accommodate for multiple SSR modes due to measurement and parametric uncertainty.

However, they are linked through the rotor’s speed deviation ($\Delta \omega_r$) and the electromagnetic torque deviation ($\Delta T_e$) that the rotor experiences under a small-signal perturbation. Since the turbine dynamics are inherently slow, the mechanical torque ($\Delta T_m$) is considered as constant. This can be seen in the block diagram shown in Fig. 20, where the electric network forms a feedback path from $\Delta \omega_r$ to $\Delta T_e$, resulting in a closed-loop system. This includes the transfer functions of the electrical system $G_e(s)$ (which comprises the series-compensated ac system, $G_s(s)$, and the VSC-HVDC link, $G_{VSC}(s)$) and of the mechanical system $G_m(s)$. The open-loop transfer functions $G_e(s)$ and $G_m(s)$ can be considered as stable to represent a practical case [10], [38]. System stability for the open and closed-loop systems can be determined using eigenvalue or electric damping analysis [10], [21].

An alternative approach is to use the positive net damping criterion [38]. In the frequency domain, the electrical and mechanical system dynamics can be expressed as

$$\frac{1}{G_e(j\omega)} = D_e(\omega) + jK_e(\omega)$$

where $D_e(\omega)$ and $D_m(\omega)$ are referred to as the electrical damping and the mechanical damping, respectively. The criterion states that if $D_e(\omega) + D_m(\omega) > 0$, then the closed-loop system in Fig. 20 is asymptotically stable. This implies a low risk of SSR [38]. For realistic systems, $D_m(\omega) > 0$. However, when series-compensated transmission lines and power electronic converters are considered, $D_e(\omega)$ may become negative for low frequencies. For these conditions, the criterion indicates a potential risk of SSR [10], [38].

The electric damping profile of the test system shown in Fig. 1 has been obtained to identify the risk of SSR using the positive net damping criterion, with results shown in Fig. 21. As it can be observed, the damping profile for Cases A (40%) and B (75%) are considerably improved when the SSFDC is in service. This is achieved as a positive electrical damping is introduced to the system. When the SSFDC is not in operation, the electrical damping is negative around the TM frequencies.
As a result, the real-time SSF identification scheme could be applied to any power system in spite of uncertainties in turbogenerator mechanical data.

The SSR phenomenon has been characterized using a reduced GB transmission network incorporating infrastructure reinforcements in the form of series compensation and HVDC links. The risk of SSR occurrence when series compensation is deployed clearly highlights the need for effective countermeasures so as to maintain the security of the network.

The contribution of an HVDC link towards SSR damping has been assessed via eigenanalysis following system state-space modeling. Results illustrate the regions of stability for different compensation levels, with stability being attained regardless of the amount of series compensation once the damper is employed. The proposed SSFDC adapts to different network conditions by acting on the unstable regions of operation resulting from the excitation of different mechanical modes at different compensation levels. An important aspect of the proposed scheme is that the HVDC link is capable of damping SSR upon occurrence while utilizing its existing capacity.

The damping scheme has been tested and validated using time-domain simulations and an RT-HIL experimental platform. Results show that damping provision does not interfere with the power transfer capability or the stability of the HVDC link. The provision of active power transfer and SSR damping using the same link rather than incorporating additional damping devices represents an enhanced asset utilization.

Even when the proposed solution works well, practical limitations requiring further investigation have been identified. The time to detect SSR and the precision of the frequency estimation rely on the signal processing method employed. In this work, FFT was adopted; however, other less computationally intensive and more efficient signal processing techniques may be employed instead. It should be also highlighted that local measurements are used as input signals to the dampers and current injection is performed by the VSC closest to the generator exhibiting torsional oscillations. If a VSC station is not sufficiently close, the damping controllers proposed in this work could be implemented in a nearby power electronic-based controller (e.g., an SVC, a TCSC or a STATCOM).

APPENDIX

A. Mathematical Derivation for SSF Component Estimation

A generator connected to a compensated line is considered to derive an expression of the voltage’s subsynchronous component at the generator terminals. The interaction between electrical and mechanical systems is simulated by applying an oscillating speed to the generator shaft. The voltage (p.u.) at the generator terminals can be written in the αβ-frame as [3], [37]

\[ e^{αβ}(t) = e^α(t) + je^β(t) \]

\[ = ω(t)E_g\cos(ω_m t + δ(t)) + jω_m E_g \sin(ω_m t + δ(t)) \]

where \( E_g \) is the amplitude of the terminal voltage at rated speed, \( δ \) its phase displacement, \( ω \) the rotor speed (p.u.), and \( ω_m \) the fundamental frequency (rad/s). If the rotor oscillates around \( ω_0 \), its speed (p.u.) is given by

\[ ω(t) = ω_0 + A \sin(ω_m t) \]

where \( A \) is the amplitude of the oscillation and \( ω_m \) is the oscillation frequency (in radians per second) of the rotor. Substituting (6) in (5), the α-axis component of the output voltage is written as

\[ e^α(t) = [ω_0 + A \sin(ω_m t)]E_g\cos(ω_m t + δ(t)) \]

\[ = ω_0 E_g \cos(ω_m t + δ(t)) + \frac{AE_m}{2}(- \sin(ω_0 - ω_m)t + δ(t)) + \sin(ω_0 + ω_m)t + δ(t)] \]

The derivative of the rotor angle \( δ \) is given as

\[ \frac{dδ(t)}{dt} = [ω(t) - ω_0]ω_B = A \sin(ω_m t)ω_B \]

where \( ω_B \) is the base frequency (rad/s). Substituting (6) in (8) yields

\[ δ(t) = δ_0 - A \frac{ω_B}{ω_m} \cos(ω_m t) \]

where \( δ_0 \) is the rotor angle in steady-state. Following the substitution of (7) in (9) and after performing algebraic reductions,

\[ e^α(t) = ω_0 E_g \sin(ω_m t + δ_0) + \frac{AE_m}{2ω_m}[-(ω_0 - ω_m)] \sin(ω_m t) \sin(ω_0 - ω_m)] \sin(ω_m t + δ_0). \]

Similarly, β-axis component of the voltage is obtained as

\[ e^β(t) = ω_0 E_g \sin(ω_m t + δ_0) + \frac{AE_m}{2ω_m}[-(ω_0 - ω_m)] \cos(ω_m t) \sin(ω_0 - ω_m)] \cos(ω_m t + δ_0). \]

Assuming the amplitude of \( A \) is small, it can be seen from (10) and (11) that the induced voltage in the stator has three terms: one at rated frequency and two at frequencies \( ω_0 ± ω_m \). The subsynchronous components are

\[ e^α_{sub}(t) = \frac{AE_m}{2ω_m}[(ω_0 - ω_m) \sin(ω_m t) + δ_0] \]

\[ e^β_{sub}(t) = \frac{AE_m}{2ω_m}[(ω_0 - ω_m) \cos(ω_m t) + δ_0] \]

Voltages in (12) cause SSF current to flow. Current components are given by

\[ i_{α_{sub}}(t) = Z^{-1}[j(ω_0 - ω_m)](e^α_{sub}(t) + je^β_{sub}(t)) \]

Finally, the torque component due to the subsynchronous current is given by

\[ T^α_{sub} = E_g i^α_{sub} \]

\[ = - \frac{AE_m}{2ω_m Z_{sub}}(ω_0 - ω_m) \sin(ω_m t + δ_{sub}). \]

From (15), the SSF component can be estimated by applying suitable signal processing approaches [20]. To this end, (3) is written as

\[ i^α(t) = \frac{1}{x(t)} \int i^α_{sub}(t) dt + \frac{1}{x(t)} \int i^β_{sub}(t) dt \]

With reference to Fig. 4 and (17), the subsynchronous component is isolated from the line current by removing the fundamental and supersynchronous components. This is achieved with a low-pass filter and a comparator block. The remaining component is \( i^α_{sub}(t) \), shown in (15). It is assumed that this is a continuous periodic real-valued signal \( y(t) \), which can be expanded using

\[ \sin(ω_m t) = \frac{2ω_m}{y(t)} \]

\[ \cos(ω_m t) = \frac{2ω_m}{y(t)} \]

\[ \tan(ω_m t) = \frac{2ω_m}{y(t)} \]
a Fourier series. The general form of the Fourier series can be written as

\[ F(t) = \sum_{k=0}^{k_0} g(t)e^{j\omega_km_t}, \quad g(t) = \frac{1}{T} \int_{0}^{T} g(t)e^{-j\omega_km_t} \]  

(18)

The Fourier transform of (18), sampled at \( F_s \) for a window length of \( T \) from \( k_0 \) to \( k_0 + T \), can be written as

\[ F(\omega) = \sum_{n=0}^{k_0+T} g(n)e^{-j\omega_km} \]

(19)

The frequency spectrum will have \( m \) peaks—one for each torsional oscillation frequency component [14]. The Fourier transform for each mode becomes

\[ F(\omega) = \sum_{n=k_0}^{k_0+T} A_n e^{-j\omega_nm} \cos(\omega_km + \phi_n) e^{-j\omega_km} \]

(20)

\[ F(\omega) \] in (20) is used to calculate the oscillatory frequency \( f_i \) in Fig. 4, where

\[ f_i = \frac{\arg \max_{g(\omega)}|F(\omega)|}{2\pi} \]

(21)

Equations (17)-(21) provide the mathematical implementation of the SSFE process described in Section II.

B. Control System Parameters of the VSC-HVDC Link

The PI controllers are represented in the form:

\[ K(s) = K_p + K_i/s \]

Two-level VSCs: Current: \( K_p = 50 \), \( K_i = 15000 \). DC voltage: \( K_p = 0.2 \), \( K_i = 200 \).

Active power: \( K_p = 0.2 \), \( K_i = 200 \). Reactive power: \( K_p = 0.2 \), \( K_i = 200 \).

REFERENCES


[31] C.E. Ugalde-Loo, J.B. Ekanayake and N. Jenkins, "Subsynchronous resonance on series compensated transmission lines with quadrature


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